Attorney Docket No. 5649-874

**PATENT** 

2815 #6/IDS DKing 5/14/02

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hag-ju Cho Serial No.: 09/893,035

Group Art Unit: 2813 Examiner: Erik J. Kielin

Filed: June 27, 2001

For: METHODS OF MANUFACTURING INTEGRATED CIRCUIT DEVICES

THAT INCLUDE A METAL OXIDE LAYER DISPOSED ON ANOTHER LAYER TO PROTECT THE OTHER LAYER FROM DIFFUSION OF

IMPURITIES AND INTEGRATED CIRCUIT DEVICES

MANUFACTURED USING SAME

Date: April 24, 2002

Commissioner for Patents Washington, DC 20231

## INFORMATION DISCLOSURE STATEMENT

Sir:

The documents listed on the attached PTO-1449 were cited in the Notice to Submit Response of corresponding Korean Application No. 10-2000-0035708. Each document listed on the attached PTO-1449 was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement. A copy of each document, including the Notice to Submit Response, is enclosed. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

These items are cited herein because they were cited in the Notice to Submit Response of the corresponding Korean application. The Examiner may also wish to consider the notations on the Notice itself regarding the relevance of each item. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR 1.97 and Section 609 of the MPEP.

Respectfully submitted,

D. Scott Moore

Registration No. 42,011

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In re: Hag-ju Cho Serial No.: 09/893,035 Filed: June 27, 2001

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PATENT TRADEMARK OFFICE

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## **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on April 24, 2002.

Traci A. Brown

Date of Signature: April 24, 2002